

**ABSTRACT**

A receiving device (50) is provided to allow appropriate clock regeneration even for a VBR TS when a stream including video and audio data, such as an MPEG2 TS, is transmitted or received in real time through a network having jitter. When the received packet data is stored in a memory (53) and the packet data is output in accordance with time information added to the received packet data, a clock frequency deviation between a sending device and a receiving device is calculated on the basis of the integration result of the amount of the received packets temporarily stored in the memory (53), the number of the received packets which have been processed, and the measurement result of the integration time used for integrating the amount of the received packets. A read timing offset of the received packet is obtained on the basis of the calculation result of the clock frequency deviation.